Bahria University,

Karachi Campus

A picture containing text, room

Description automatically generated

LAB EXPERIMENT NO:

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09

LIST OF TASKS

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| TASK NO | OBJECTIVE |
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| 01 | Verify the functionality of NOT, AND, OR, XOR, NAND, NOR, and XNOR Gates using MultiSim by designing the gates and creating their associated Truth Tables. Attach circuit snapshots and Truth Table illustrations for verification. |
| 02 | Implement the following equations on MultiSim and verify through Truth Table. (Attach the circuit snapshots)   1. (A + B) . (A . C) 2. A + B + B.C .(B+C) 3. AB’CD + A (B + C) + BC’D + ABD + BC’ 4. (A + B’ + C ) . (A’ + C’ + D) . (B + C + D’) 5. (A’) . (A + B) + (B + A) . (A + B’) |
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Submitted On:

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/12/23

(Date: DD/MM/YY)